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PATENT APPLICATION
Docket No.: NC 84,781

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of: Dennis

Serial No.: 09/715,772

Filed: 11/17/2000

For: MULTI-THREAD PERIPHERAL PROCESSING USING DEDICATED PERIPHERAL
BUS

Examiner: King, Justin

Art Group Unit: 2181

Honorable Commissioner of Patents

PO Box 1450

Alexandria, VA 22313-1450

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RESPONSE TO FINAL REJECTION

Sir:

In response to the Office action of 07/19/2005, setting a three month shortened statutory period of response, please consider the following remarks.

Claims 1-41 are pending in the application. No claims are presently allowed.

Claim Rejections – 35 U.S.C. § 102

Claims 1-12, 14-25, 27-38, and 41 have been rejected under 35 U.S.C § 102(b) as allegedly anticipated by Motomura (US 5,815,727).

Claim 1 recites an apparatus comprising: a peripheral bus coupled to a peripheral unit and a processing slice coupled to the peripheral bus. The peripheral bus transfers peripheral information including a command message specifying a peripheral operation. The processing slice executes a plurality of threads comprising instructions. The threads include a first thread sending the command message to the peripheral unit. The processing slice comprises a functional unit to perform a register operation specified in the instructions dispatched from each

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I certify that this correspondence is being facsimile transmitted to the US Patent and Trademark Office on the date shown below.

8/10/05
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Joseph T. Grunkemeyer